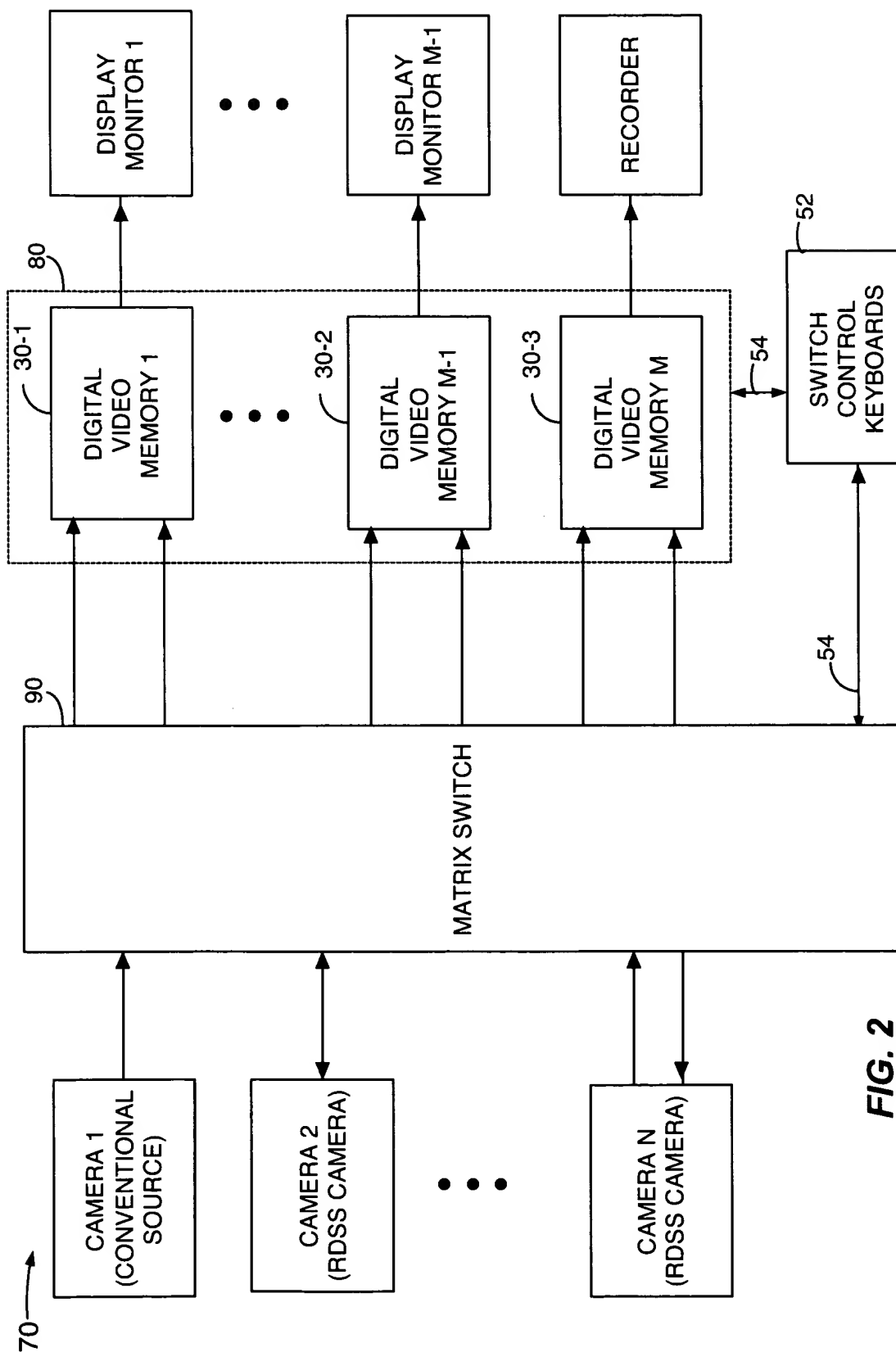


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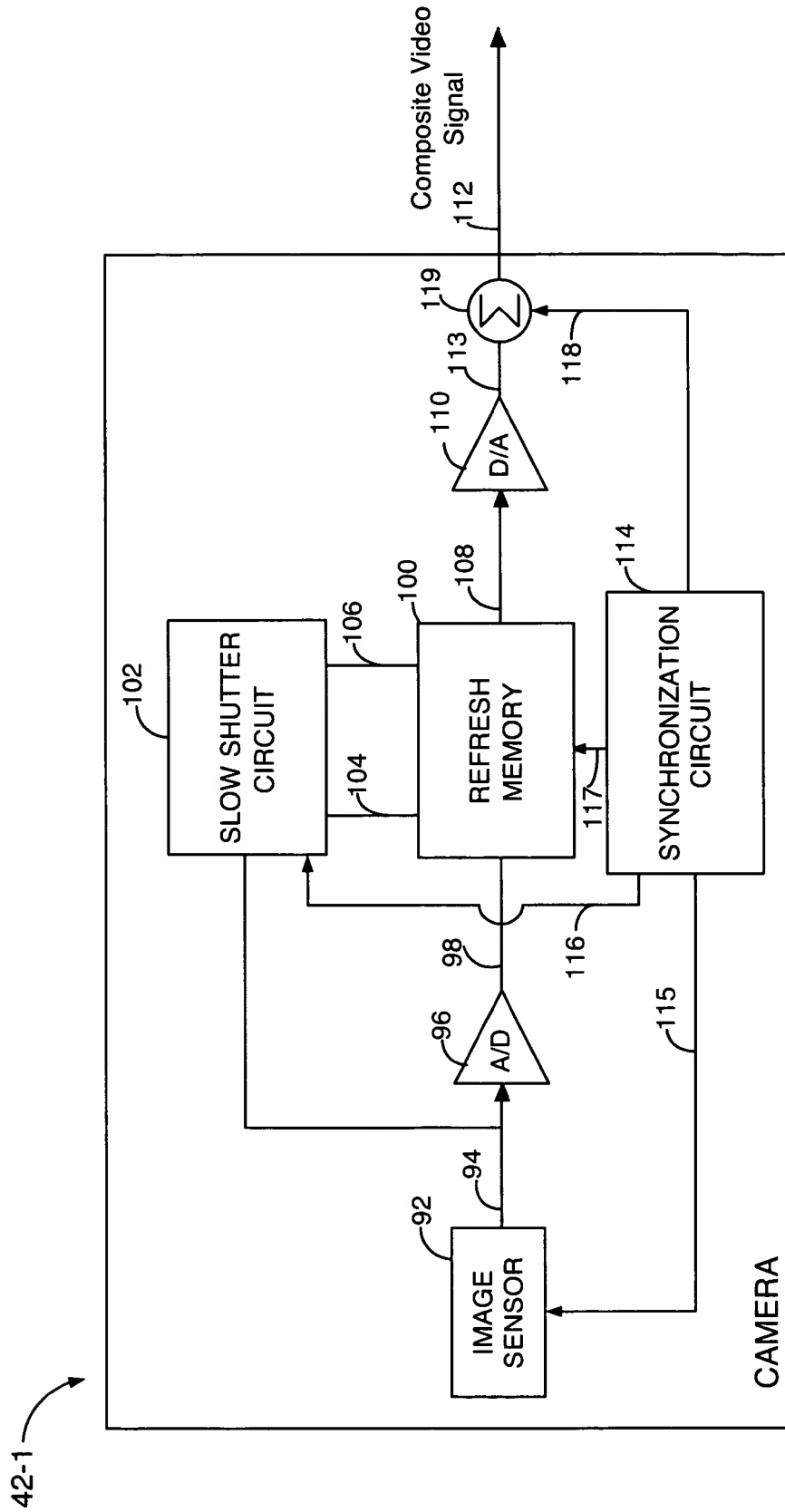


FIG. 3 PRIOR ART

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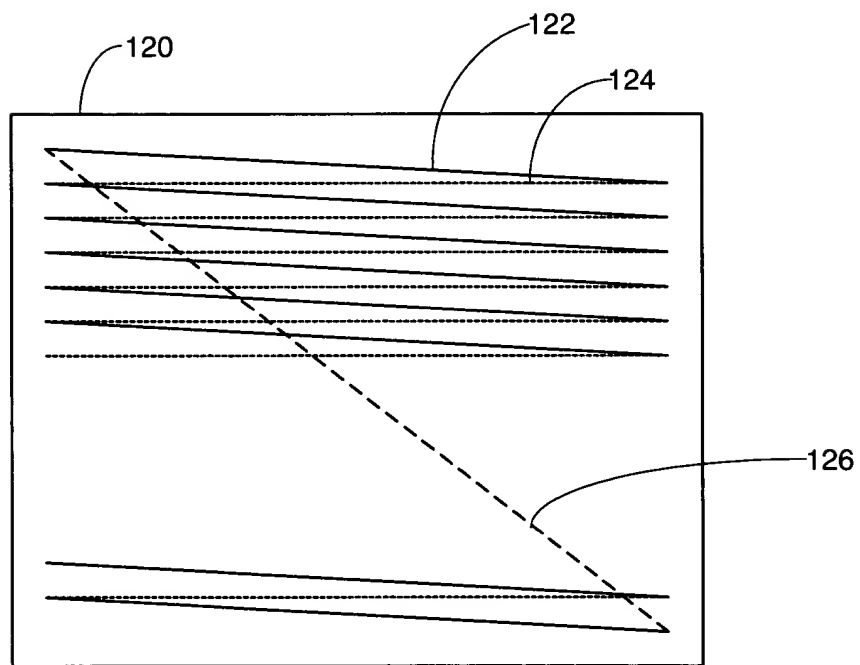
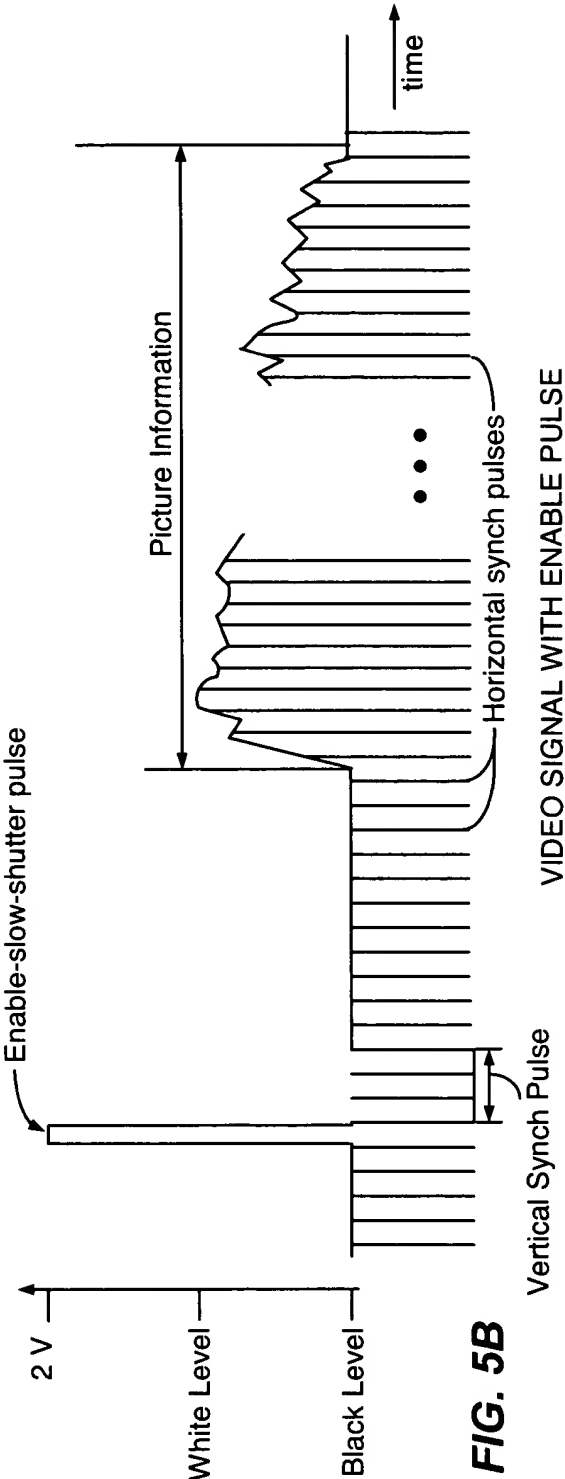
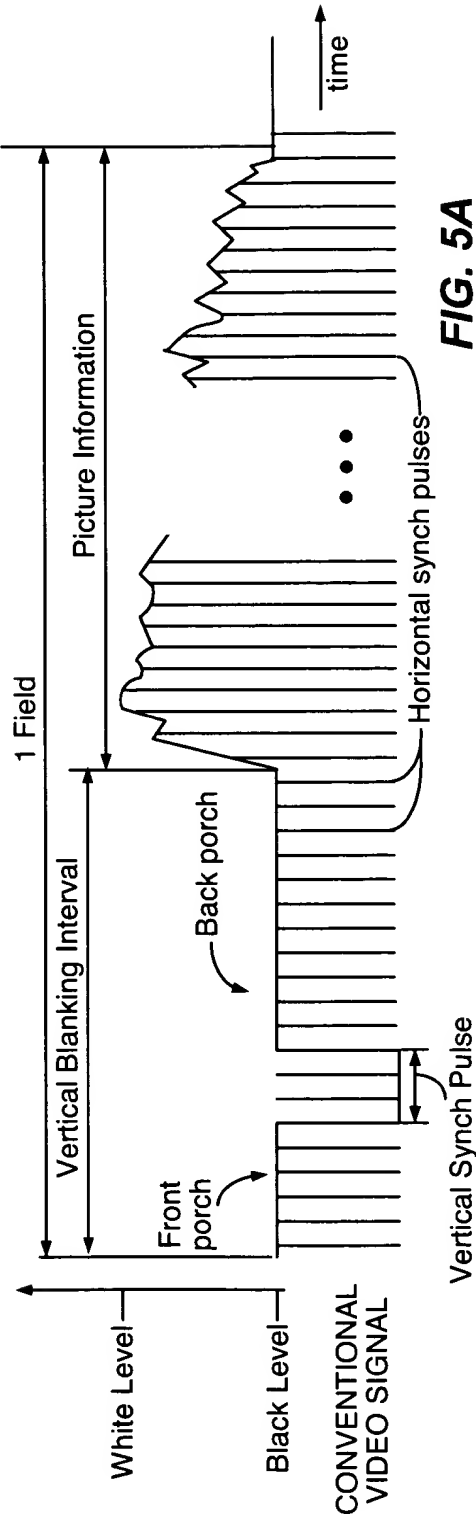
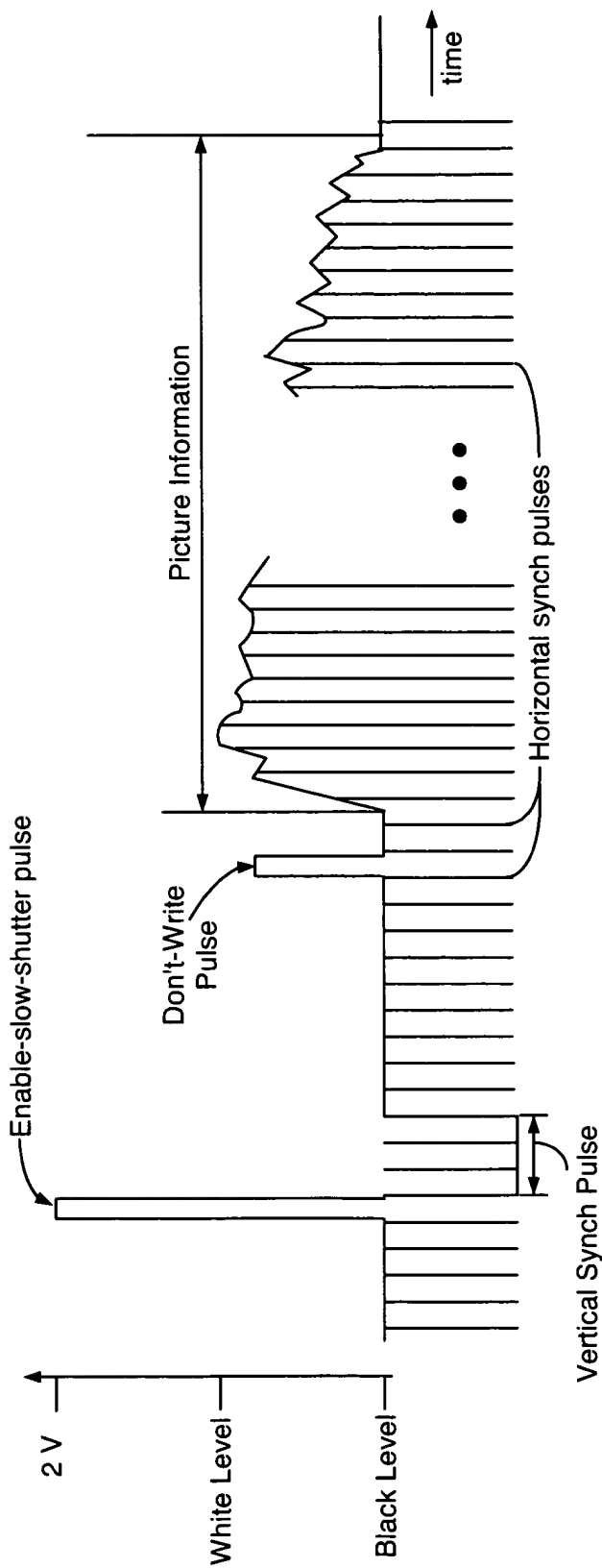


FIG. 4



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VIDEO SIGNAL WITH DON'T-WRITE PULSE
FIG. 5C

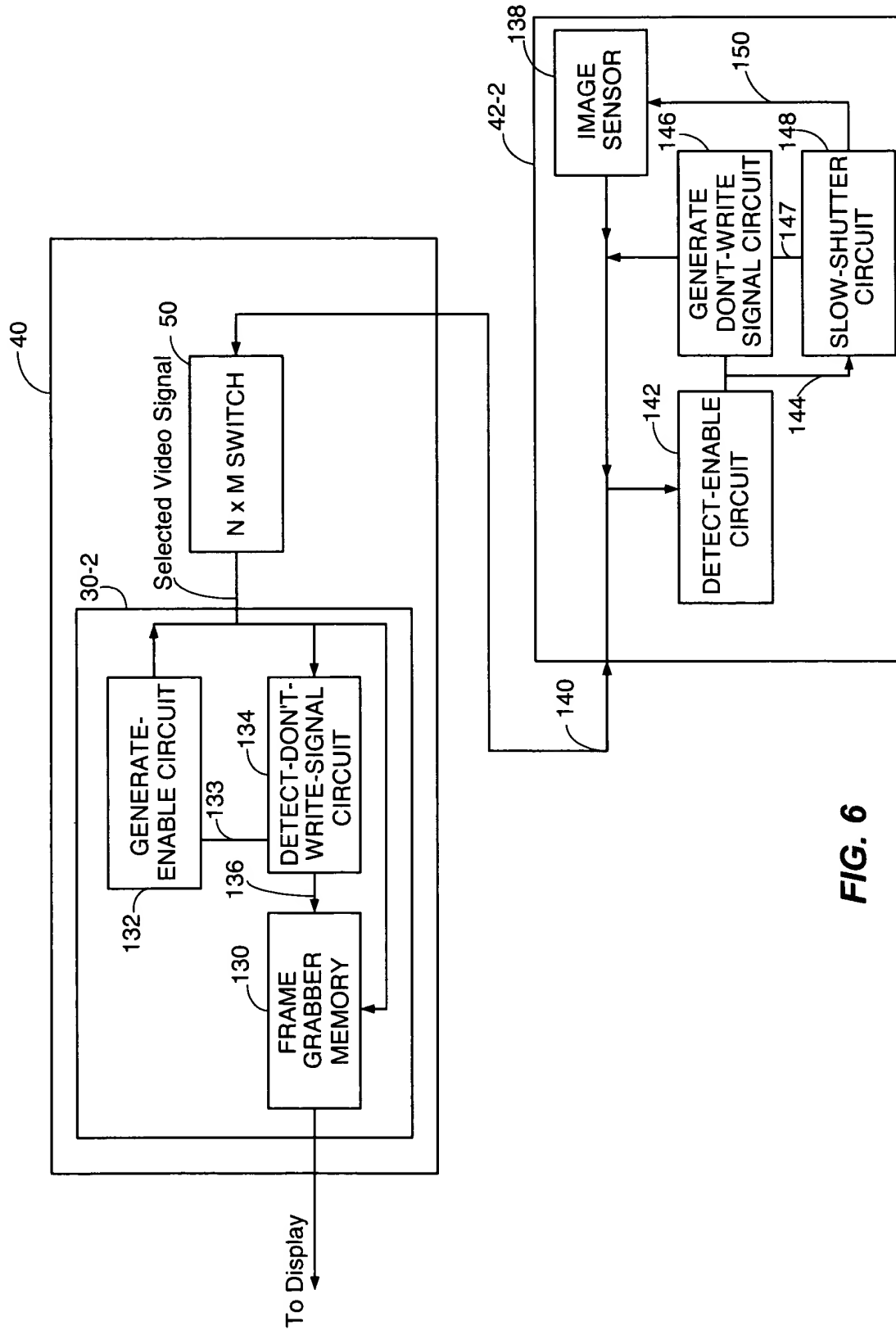
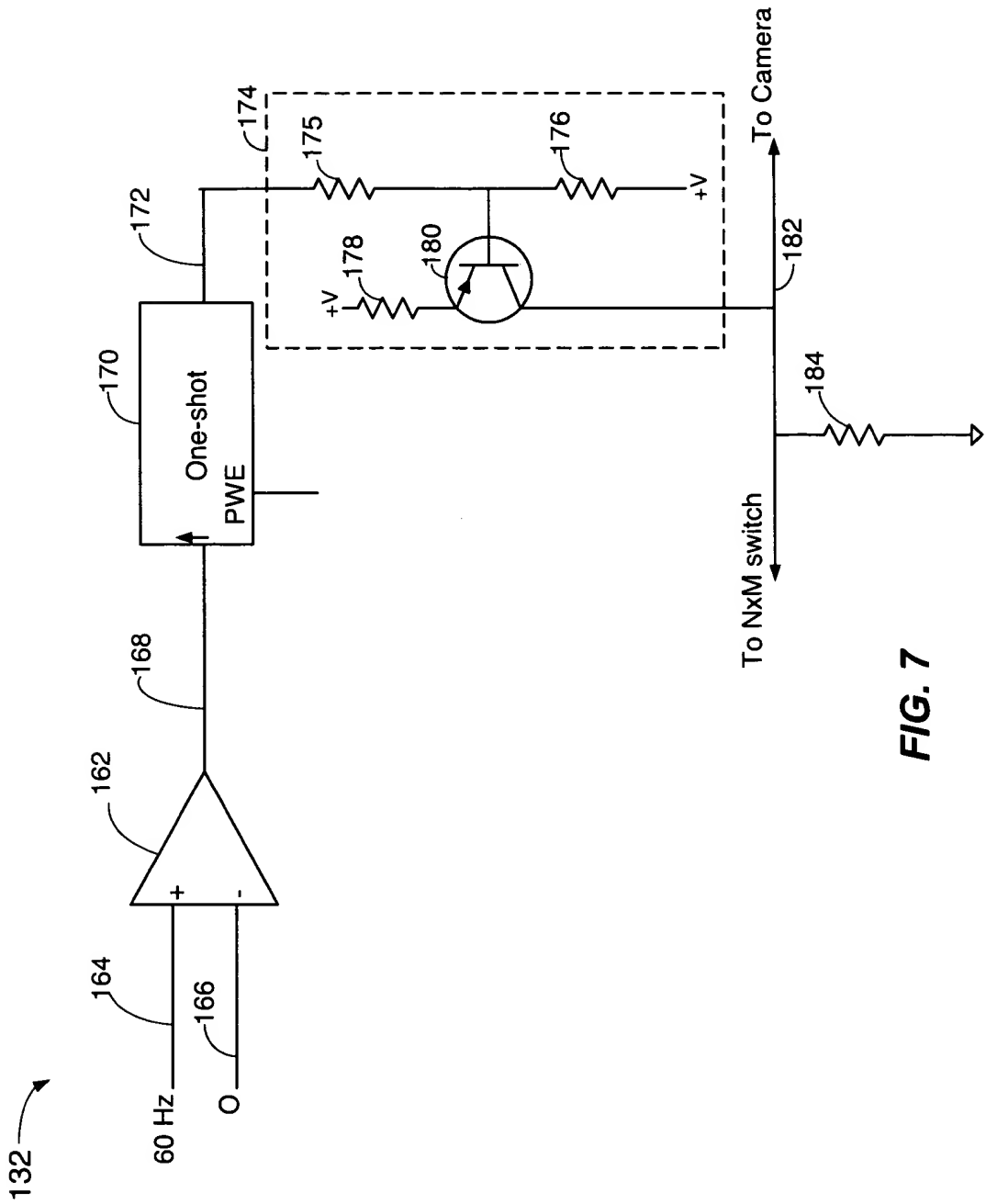
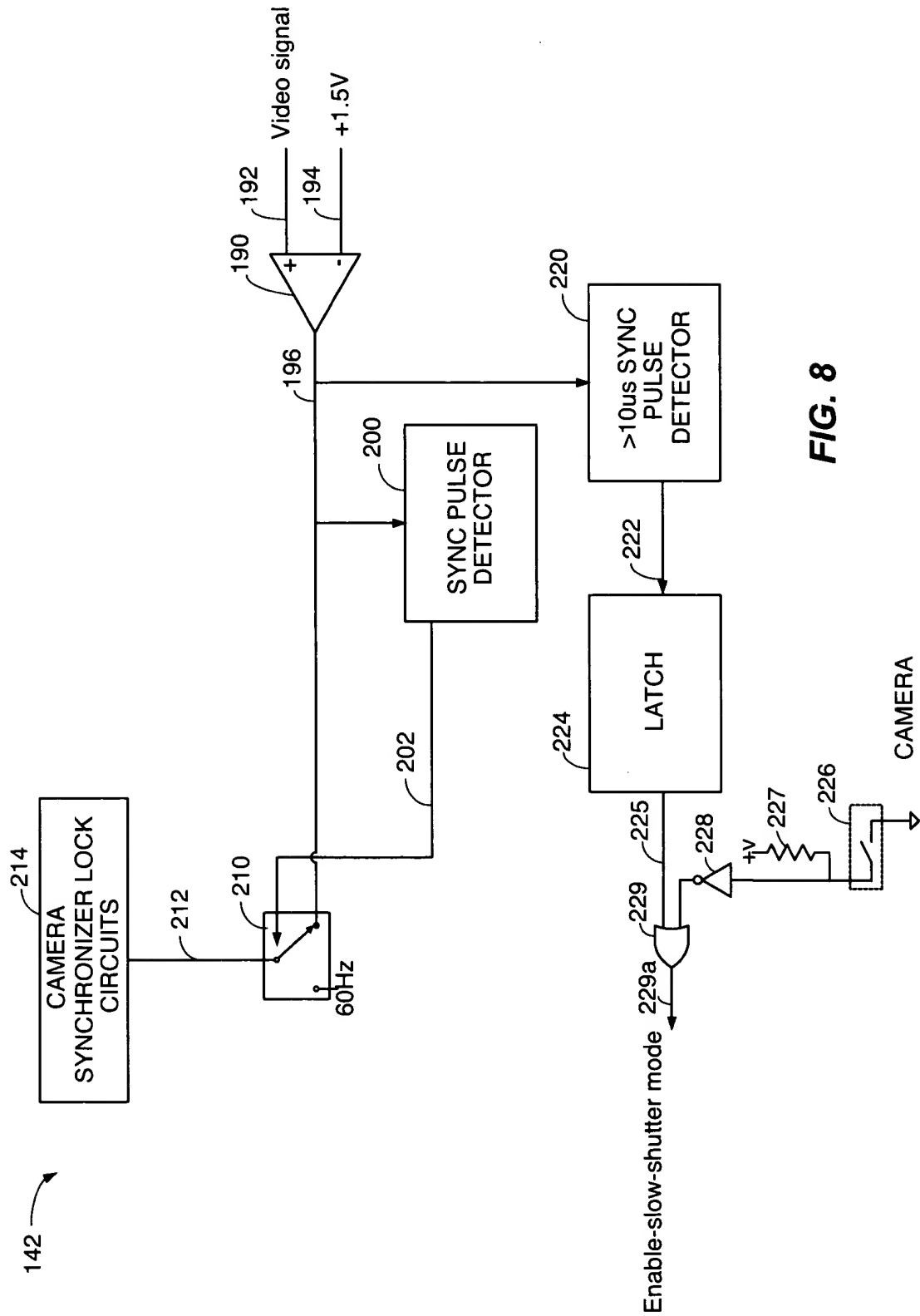


FIG. 6

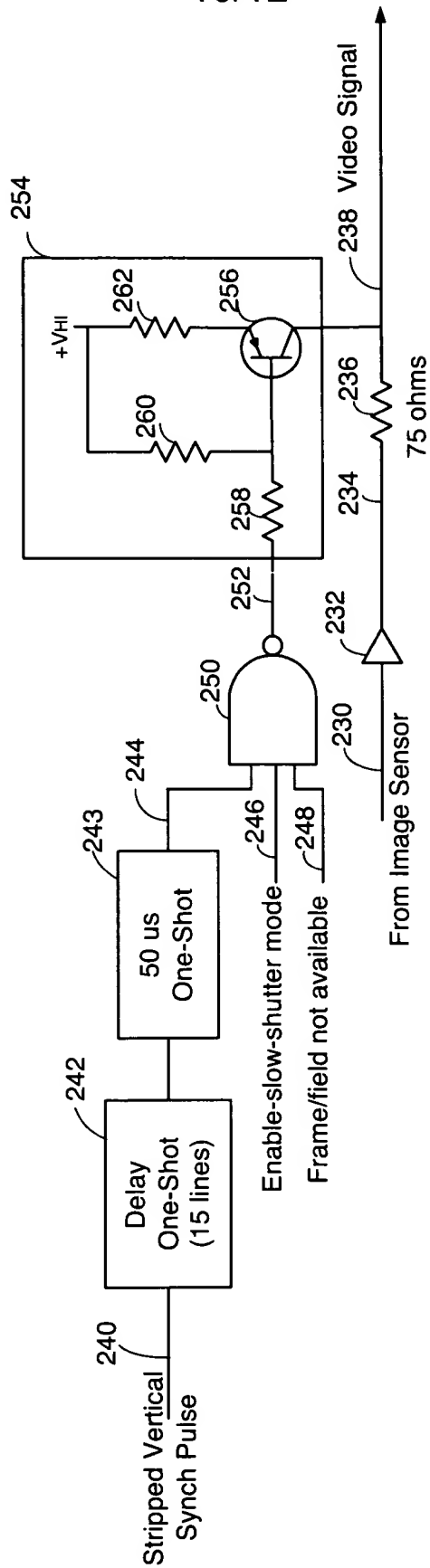


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GENERATE DON'T WRITE SIGNAL CIRCUIT

FIG. 9

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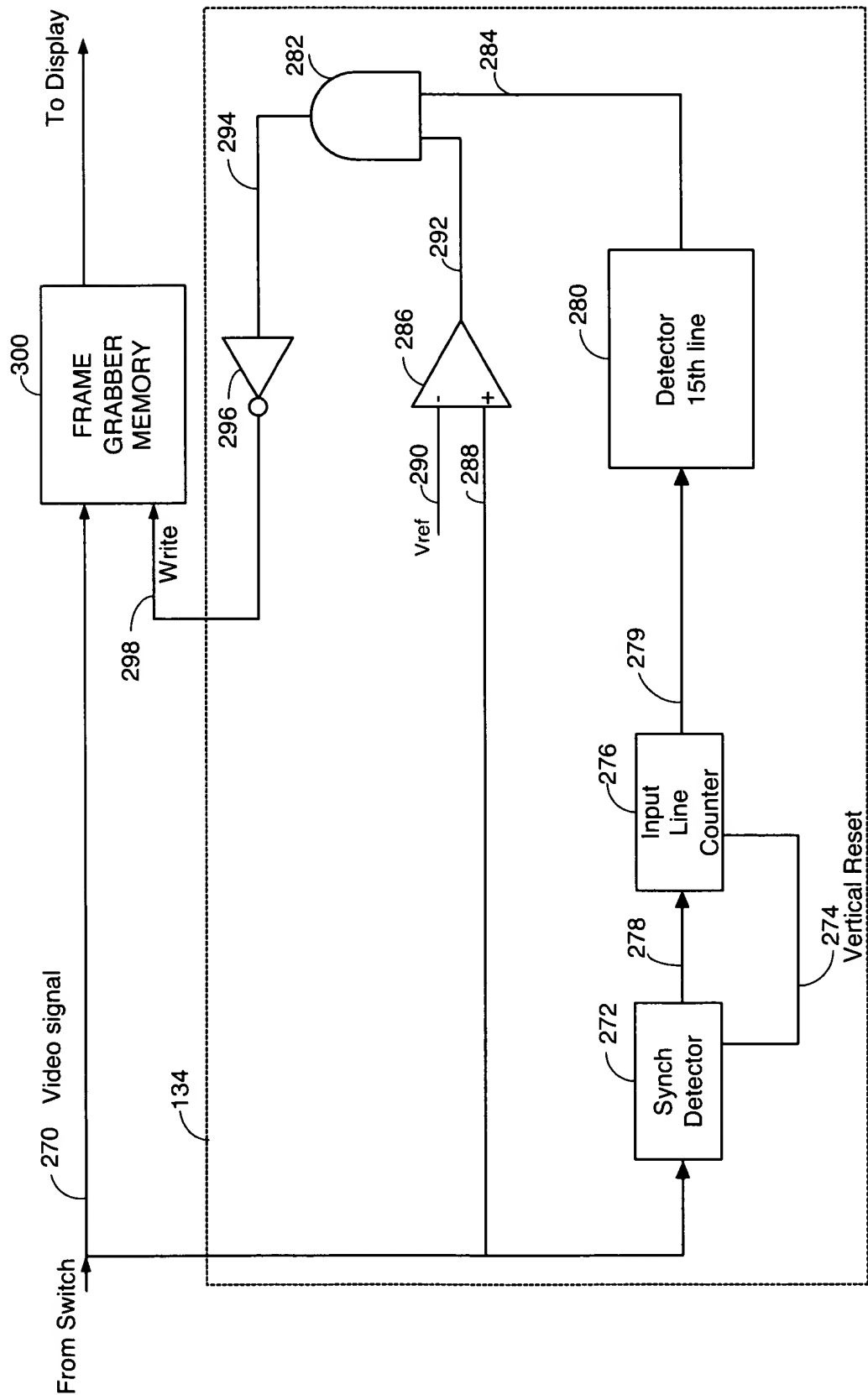


FIG. 10

DIGITAL VIDEO MEMORY WITH THE DETECT-DON'T-WRITE SIGNAL CIRCUIT

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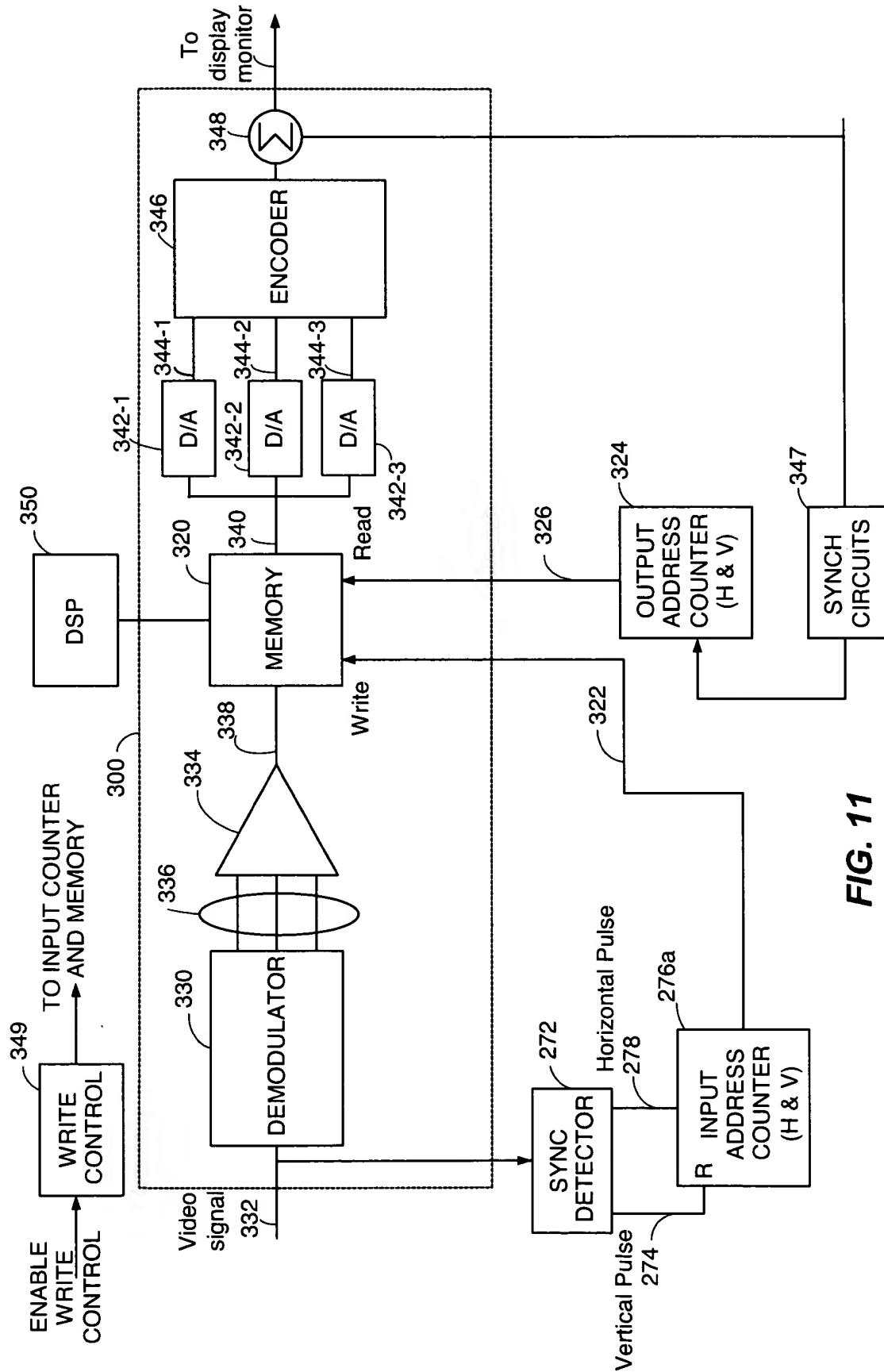


FIG. 11